

CLAIMS:

1. A microcontroller (100) comprising a command decoder (15) and at least one memory (14) to which the command decoder has access, the command decoder being adapted to decode at least one conditional command, such that the result of the decoding of the conditional command is dependent on the contents of said memory.

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2. A microcontroller as claimed in claim 1, characterized in that the memory (14), to which the command decoder has access, is adapted in such a way that it can include input data of the microcontroller.

3. A microcontroller as claimed in any one of claim 1 or 2, characterized in that the command decoder (15) is adapted in such a way that, dependent on the contents of the memory (14), to which the command decoder has access, a conditional command is decoded as an access command for the computer (12) to different memory types.

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